

SIDC16D60SIC3

Silicon Carbide Schottky Diode

FEATURES:

- Worlds first 600V Schottky diode
- Revolutionary semiconductor material -Silicon Carbide
- Switching behavior benchmark
- No reverse recovery
- No temperature influence on the switching behavior
- Ideal diode for Power Factor Correction
- No forward recovery

Applications:

• SMPS, PFC, snubber



Chip Type	V_{BR}	I _F	Die Size	Package	Ordering Code	
SIDC16D60SIC3	600V	5A	1.26 x 1.26 mm ²	sawn on foil	Q67050-A4271-	
010010000100	0000	34	1.20 X 1.20 IIIIII	A101		

MECHANICAL PARAMETER:

Raster size	1.26 x 1.26				
Anode pad size	0.960 x 0.960	mm			
Area total / active	1.588 / 0.96	mm ²			
Thickness	355	μm			
Wafer size	75	mm			
Flat position	0	deg			
Max. possible chips per wafer	2457 pcs				
Passivation frontside	Photoimide				
Anode metalization	3200 nm Al				
Cathode metalization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding				
Die bond	electrically conductive glue or solder				
Wire bond	AI, ≤ 125μm				
Reject Ink Dot Size	Ø ≥ 0.2 mm				
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				



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Maximum Ratings

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	V_{RRM}		600	V
Surge peak reverse voltage	V _{RSM}		600] *
Continuous forward current limited by T_{jmax}	I _F		5	
Single pulse forward current (depending on wire bond configuration)	I _{FSM}	T_C =25°C, t_P =10 ms sinusoidal	18.5	A
Maximum repetitive forward current limited by T _{jmax}	I _{FRM}	$T_C = 100^{\circ}C, T_j = 150^{\circ}C,$ D = 0.1	21	
Non repetitive peak forward current	I _{FMAX}	$T_C = 25^{\circ}C$, $tp = 10\mu$ s	50	
Operating junction and storage temperature	$T_{\rm j}$, $T_{ m stg}$		-55+175	°C

Static Electrical Characteristics (tested on chip), T_i =25 °C, unless otherwise specified

Parameter	Symbol	Condi	Value			Unit	
raiailletei	Syllibol	Condi	itions	min.	Тур.	max. 200	Oiiit
Reverse leakage current	I_{R}	V _R =600V*	<i>T_j</i> =25 °C		19	200	μΑ
Forward voltage drop	V _F	I _F =5A	T _j =25°C		1.5	1.7	V

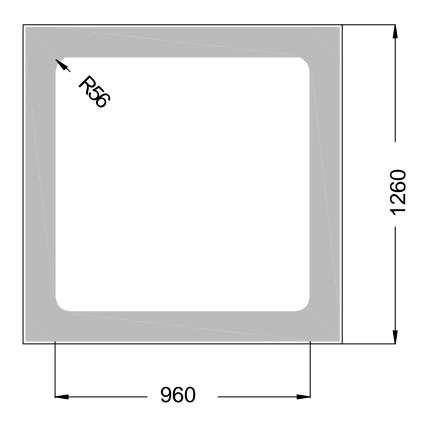
^{*} blocking characteristic measured under protective gas atmosphere. Chip should not be used without being embedded in pottant with breakdown field strength lower than 9 KV/mm at full blocking voltage.

Dynamic Electrical Characteristics, at $T_j = 25$ °C, unless otherwise specified, tested at component

Parameter	Symbol Condi		tions	Value			Unit
raiailletei	Syllibol	Condi	Conditions		Тур.	max.	7 01111
Total capacitive charge	Q_C	I_F =5A di/dt=200A/ms V_R =400V	$T_j = 150 ^{\circ}\text{C}$		14		nC
Switching time	t _{rr}	$I_F=5A$ di/dt=200A/ms $V_R=400V$	T _j = 150 °C		n.a.		ns
Total capacitance	С	I _F =5A di/dt=200A/ m s	V _R =1 V		170		
		$T_j=25$ °C f=1MHz	V _R =300V		16		pF
			V _R =600V		12		



CHIP DRAWING:





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FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the device data sheet s

Description:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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